REMARKS

1. This paper is responsive to the Office Action mailed June 20, 2001. Reconsideration
and further examination is respectfully requested. Claims 10 and 11 have been
amended. Claims 1-9 have been cancelled. Claims 10,11, and 12 remain. Pages 3
and 4 of the specification and the Abstract have been amended to correct errors. No
new matter has been added.

2. Page 3, paragraph 2 and the Abstract have been amended to correct errors that are
similar to each other. No new matter has been added. Specifically, line 17 of page
two has been amended to replace "reset, or precharge," with --evaluate--. A similar
change was made to line 8 of the Abstract.

   The correction of these errors do not constitute new matter because page 3,
lines 11-14, and the abstract of the disclosure, clearly state that "the inverse of the
dynamic logic evaluate clock is fed to the clock input of a transparent latch" and that
"a delayed version of [the inverse of the dynamic logic evaluate clock] is fed to the
enable input of the latch." Page 3 line 17 states that the latch "remains closed until a
delay time after the evaluate clock instructs the dynamic logic to reset." Clearly, for
the latch to remain "closed until a delay time after the evaluate clock instructs the
dynamic logic to reset" when a delayed version of the inverse of the dynamic logic
evaluate clock is fed to the enable input of the latch, the latch then the latch must be
closed during the evaluate phase of the evaluate clock. Therefore, since the latch is
closed during the evaluate phase, it only makes sense that the latch must be *open until*
the evaluate phase.

   Also, it can be seen from the schematic diagram shown in FIG. 1 that if the
clock and enable inputs of the latch in FIG. 1 (102) are fed the inverse of the dynamic
logic clock and a delayed version of that inverse (as called for on page 3 and the
abstract) that the net result is a latch that is open until the evaluate clock is instructing
the dynamic logic to *evaluate*--not reset as the specification erroneously stated.

3. Page 4, paragraph 1 has been amended to correct an error. On line 4 of page 4, the words --inverse of the-- are inserted before the second occurrence of dynamic logic. On line 5, the word "high" is replaced by --low--. These corrections do not constitute new matter because, as stated in the Abstract, the Summary of the Invention, "the inverse of the dynamic logic evaluate clock is fed to the clock input of a transparent latch" and CK is the clock input of transparent latch 102 in FIG. 1. (See page 4, line 8 that states that CK is connected to the clock input of transparent latch 102.) Also, since CK is the inverse of the dynamic logic evaluate clock, and the dynamic logic evaluate clock is high during the dynamic logic evaluate phase, CK is *low*, not high during the dynamic logic evaluate phase.

4. Page 4, paragraph 2 has been amended to correct an error. On line 13 of page 4, the word "rises" has been corrected to be "falls". This correction does not constitute new matter because as shown in the schematic of FIG. 1, it is the *falling* of the inverse of the dynamic logic clock that closes transparent latch 102.

5. The drawings were objected to under 37 CFR 1.83(a). The corrections to the specification on page 4, discussed above, traverse this objection since it is now clear that CK, as shown in FIG. 1, is "an inverse of a dynamic logic clock" and "a clock that is the inverse of a second clock".

6. Claims 1-12, 15, and 16 were rejected under 35 U.S.C. 112. In particular, certain recitations were rejected as misdescriptive because they were inconsistent with Figure 1. The corrections to the specification discussed above correct this problem.
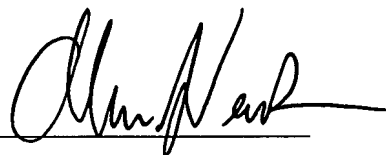
Claim 11 was rejected because it referred to itself. This problem has been corrected by an amendment to claim 11 making it dependent upon claim 10.

7. Claims 10-12 were objected to as being dependent upon a rejected base claim but the Examiner stated they would be allowable if rewritten in independent form including

all of the limitations of the base claim. Claim 10 has been amended to be in independent form and include limitations of claim 9.

8. This application is considered to be in condition for allowance and such action is earnestly solicited.

Respectfully submitted

by _Alexander J. Neudeck_

Alexander J. Neudeck
Reg. No. 41,220

September 20, 2001
Fort Collins, CO 80528-9599
(970) 898-4931